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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10075465	02/15/2002	438	149	2911 2647	GEYER

**APPLICANTS: Oyamatsu Hisato;

JC 1061

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-392571 12/25/2001

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiners's initials		219660US2S	
TITLE : Semiconductor device having patterned SOI structure and method for fabricating the same U.S. DEPT. OF COM/USPTO-TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.		Figs.Drwg.
TERMINAL		Primary Examiner		
DISCLAIMER		Application Examiner		
PREPARED FOR ISSUE				
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